

Docket No.: SON-2810

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Yoshitaka Kayukawa et al.

Application No.: 10/647,217

Confirmation No.: 1901

Filed: August 26, 2003

Art Unit: 2138

For: SEMICONDUCTOR INTEGRATED CIRCUIT

AND METHOD FOR TESTING SAME

Examiner: D. B. Gandhi

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

Applicants hereby respond to the Notice of the Non-Compliant Amendment dated October 19, 2006.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.